

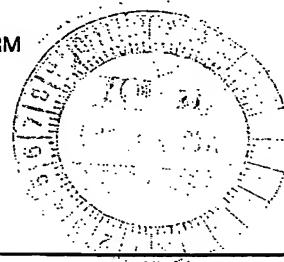
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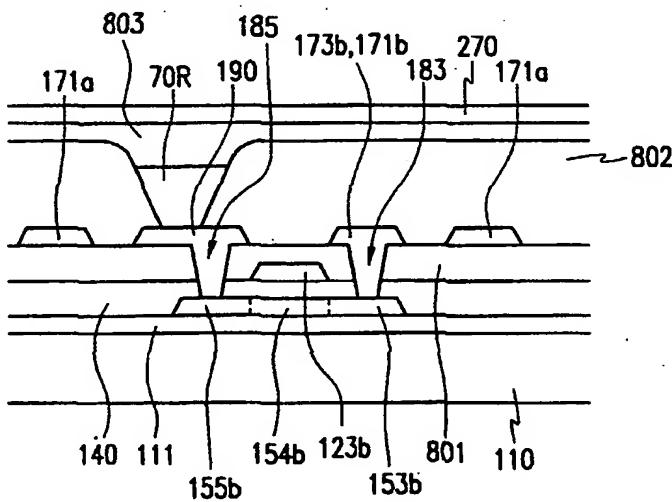
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(54) Title: **ORGANIC ELECTROLUMINESCENCE DISPLAY PANEL AND MANUFACTURING METHOD THEREOF**



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(57) Abstract: A polysilicon layer (153b, 154b, 155b) is formed on an insulating substrate (110) and a gate insulating layer (140) is formed on the polysilicon layer. A gate line (121) is formed on the gate insulating layer (140) and an interlayer insulating film (801) is formed on the gate line. A data line (171a) and a pixel electrode (190) are formed on the interlayer insulating film. A partition (802) is formed on the data line and the pixel electrode and an organic EL layer (70R) is formed on the pixel electrode in a predetermined area defined by the partition. A common electrode (270) is formed on the organic EL layer.